

	Datasheet	No.	DS10-U013
		Initial Date	2023-05-23
OU	OUB8Cxx412	Written Team	R&D Dept.
			GH Zheng

I Preview

PN	OUB8Cxx412
Description	100G QSFP28 LR1 Bi-Di 1291/1311nm 20KM LC DDMI 10~70 °C

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III Revision History

No.	Date	Items	Change Recording	Ver.	Rev.	Customer
1	2023-05-23	All	Initial registration	000	000	Standard
2						
3						
4						
5						
6						

 Communication Limited	Datasheet		DS10-U013 Final Rev.: 2023-05-23	
	Product	100G QSFP28 transceiver OU serials	Ver.	000
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1. Features:

- ◆ Supports 100GBASE-LR1-20 BIDI
- ◆ Lane signaling rate 106.25Gb/s with PAM4
- ◆ Up to 20km transmission on SMF
- ◆ EML Laser and PIN receiver
- ◆ 4x25.78Gb/s with NRZ electrical interface (CAUI-4)
- ◆ High speed I/O electrical interface
- ◆ I2C interface with integrated Digital Diagnostic monitoring
- ◆ QSFP28 MSA package with simplex LC connector
- ◆ Single +3.3V power supply Voltage
- ◆ Maximum 1.2A Power Supply Current
- ◆ Support HW TX_DIS and RX_LOS for telecom application
- ◆ Maximum power consumption 4 W
- ◆ Operating case temperature: 0 to +70 °C
- ◆ Compliant to IEEE802.3bm, 100G single Lambda MSA
- ◆ Compliant to SFF-8636&SFF-8679 standard
- ◆ Complies with EU Directive 2015/863/EU

2. Applications

- ◆ 100GBASE-LR1-20 BIDI
- ◆ Other links

3. Description

The 100G QSFP28 LR1 Bi-Di transceivers are high-performance, cost-effective modules supporting 20KM on SMF. It integrates four electrical data lanes in each direction into transmission over a single LC single fiber optic cable. Each electrical lane operates at NRZ 25 Gbps and conforms to the PAM4 100GE optical interface. All modules satisfy class I laser safety requirements. The transceivers are compatible with 100GBASE-LR1-20 standard of IEEE802.3bm and SFF-8636&SFF-8679 specification.

4. Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Supply Voltage	Vcc	-0.5	4	V
Storage Temperature	Ts	-40	+85	°C
Operating Humidity	-	5	85	%

5. Operating Environment

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Parameter	Symbol	Min	Typical	Max	Unit	
Operating Case Temperature	Tc	0		+70	°C	
Power Supply Voltage	Vcc	3.135	3.30	3.465	V	
Power supply Current	Icc	-	-	1.2	A	
Power Dissipation	Pm			4	W	
Data Rate			106.25		Gbps	
Transmission Distance	TD	-	-	20	KM	SMF

6. Optical and Electrical Characteristics

Optical						
Parameter	Symbol	Min	Typical	Max	Unit	Notes
Transmitter						
Centre Wavelength	λ_{c1}	1284.5	1291	1297.5	nm	OUB8C12412
Centre Wavelength	λ_{c2}	1304.5	1311	1317.5	nm	OUB8C21412
Average launch power	P _{out}	-0.2		+6.6	dBm	1
Outer Optical Modulation Amplitude	OMA	Max (2.8, 1.4+TDEC Q)	-	6.8	dBm	
Transmitter eye closure for PAM4 (TECQ) (max)	TECQ	-	-	3.4	dBm	
Transmitter and dispersion eye closure for PAM4 (TDECQ) (max)	TDECQ	-	-	3.6	dBm	
TDECQ-TECQ (max)	-			2.7	dB	
Extinction Ratio	ER	3.5			dB	
Average launch power of OFF transmitter, each lane	P _{off}			-30	dBm	
Side Mode Suppression Ratio	SMSR	30	-	-	dB	
Receiver						
Centre Wavelength	λ_{c1}	1304.5	1311	1317.5	nm	OUB8C12412
Centre Wavelength	λ_{c2}	1284.5	1291	1297.5	nm	OUB8C21412
Damage threshold	THd	7.6			dBm	2
Average power at receiver		-10	-	+6.6	dBm	3
Receive power_ OMA _{outer} *	POMA	-	-	6.8	dBm	
Receiver sensitivity_ OMA _{outer}	SEN	-	-	Max(-7.6, -	dBm	4

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	_OMA			9+TECQ)		
LOS De-Assert	LOS _D			-10	dBm	
LOS Assert	LOS _A	-26			dBm	
LOS Hysteresis		0.5			dB	

Electrical

Parameter	Symbol	Min	Typical	Max	Unit	Notes
Transmitter						
Input Differential Impedance	R _{in}	-	100	-	Ohm	
Differential Data Input Amplitude	V _{IN,P-P}	80	-	900	mV _{pp}	
Differential termination mismatch (max)	D-mismatch	-	-	10	%	
DC common-mode input voltage		-0.3	-	2.8	V	
Transition time(20%~80%)	Tr Tf	10	-	-	ps	
LPMODE, Reset and ModSelL / Tx dis	V _{IL}	-0.3	-	0.8	V	
LPMODE, Reset and ModSelL / Tx dis	V _{IH}	2.0	-	V _{CC} +0.3	V	
Receiver						
Output Differential Impedance	R _{out}	-	100	-	Ohm	
Differential Data Output Amplitude	V _{OUT,P-P}	-		900	mV _{pp}	
Differential termination mismatch (max)	D-mismatch	-		10	%	
Transition time, 20% to 80%	Tr Tf	12			ps	
ModPrsL and IntL/ Rx los	V _{OL}	0		0.4	V	
ModPrsL and IntL/ Rx los	V _{OH}	V _{CC} -0.5		V _{CC} +0.3	V	

- The optical power is launched into SMF.
- The receiver shall be able to tolerate, without damage, continuous exposure to an optical input signal having this average power level. The receiver does not have to operate correctly at this input power.
- Average receive power, each lane (min) is informative and not the principal indicator of signal strength.
- Measured with conformance test signal at TP3 using the test pattern PRBS31Q or scrambled idle for stressed receiver sensitivity for the BER=2.4x10⁻⁴.

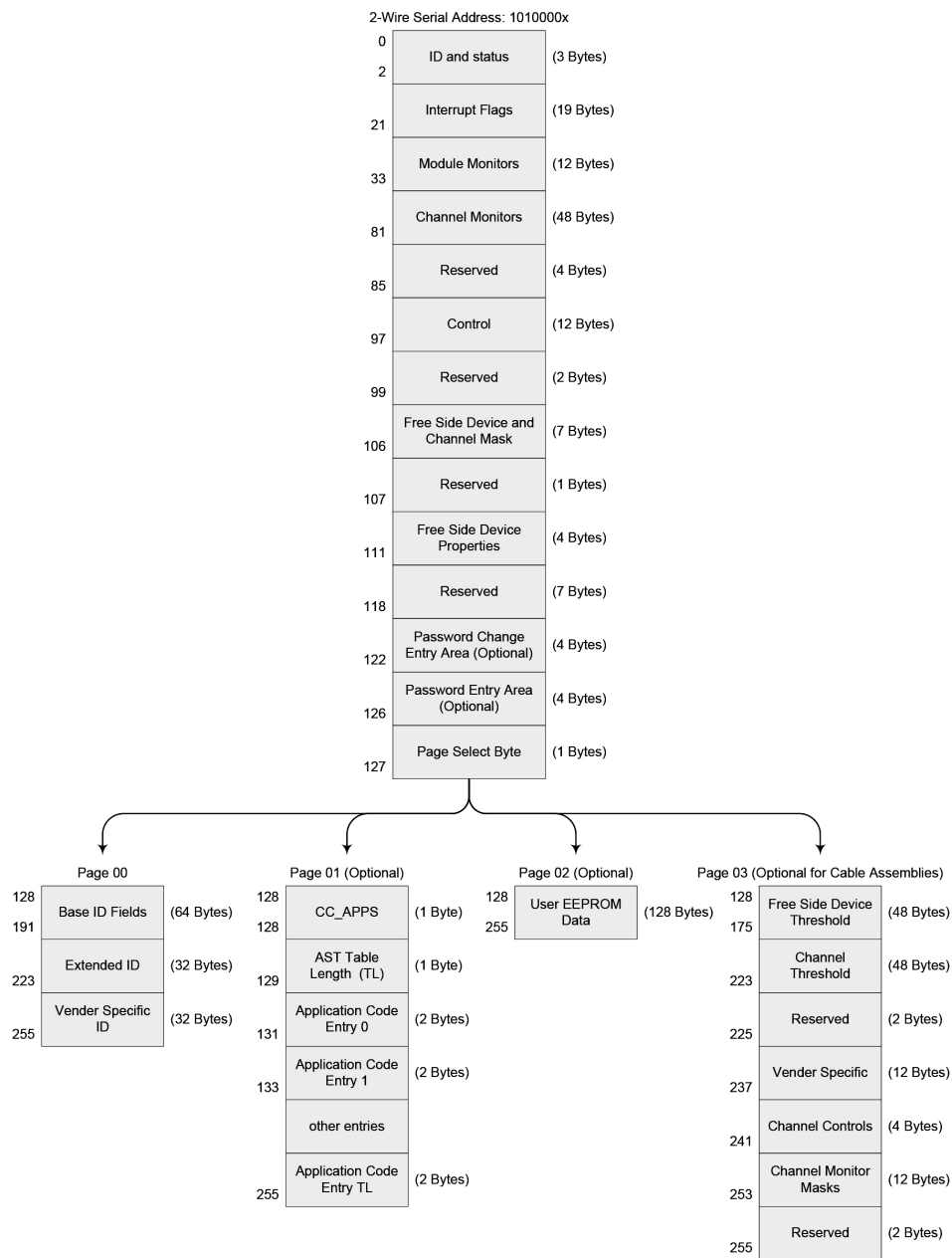
7. Digital Diagnostic Memory Map

The transceivers provide serial ID memory contents and diagnostic information about the present operating conditions by the 2-wire serial interface (SCL, SDA).

The digital diagnostic memory map specific data field defines as following.

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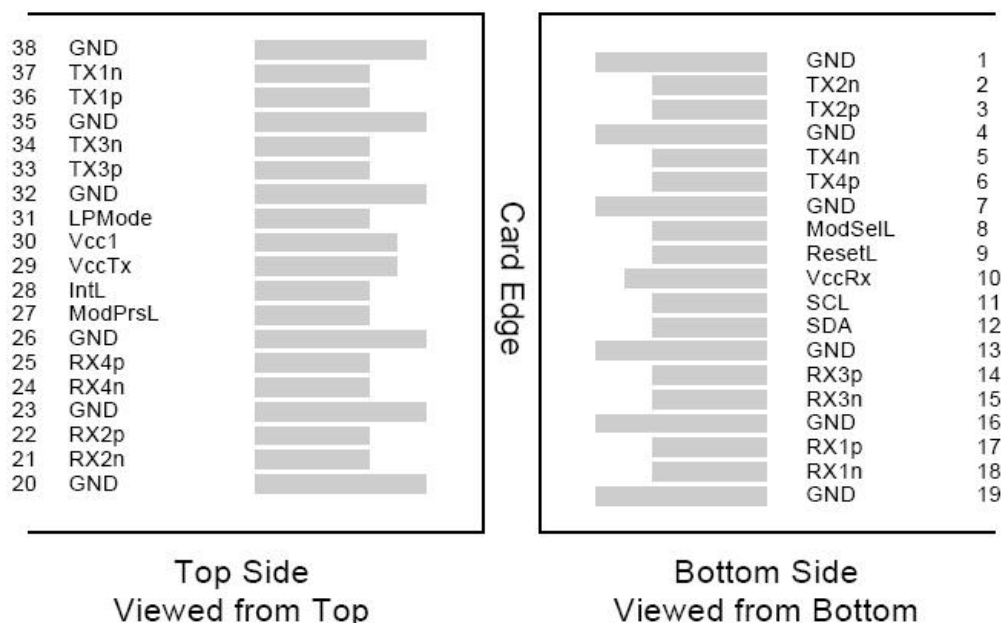
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8. Pin Descriptions



PIN	Logic	Symbol	Name/Description	Plug Seq.	Note
1		GND	Ground	1	1
2	CML-I	Tx2n	Transmitter Inverted Data Input	3	
3	CML-I	Tx2p	Transmitter Non-Inverted Data output	3	
4		GND	Ground	1	1
5	CML-I	Tx4n	Transmitter Inverted Data Input	3	
6	CML-I	Tx4p	Transmitter Non-Inverted Data output	3	
7		GND	Ground	1	1
8	LVTLL-I	ModSelL	Module Select	3	
9	LVTLL-I	ResetL	Module Reset	3	
10		VccRx	+ 3.3V Power Supply Receiver	2	2
11	LVC MOS-I/O	SCL	2-Wire Serial Interface Clock	3	
12	LVC MOS-I/O	SDA	2-Wire Serial Interface Data	3	
13		GND	Ground	1	
14	CML-O	Rx3p	Receiver Non-Inverted Data Output	3	
15	CML-O	Rx3n	Receiver Inverted Data Output	3	
16		GND	Ground	1	1

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17	CML-O	Rx1p	Receiver Non-Inverted Data Output	3	
18	CML-O	Rx1n	Receiver Inverted Data Output	3	
19		GND	Ground	1	1
20		GND	Ground	1	1
21	CML-O	Rx2n	Receiver Inverted Data Output	3	
22	CML-O	Rx2p	Receiver Non-Inverted Data Output	3	
23		GND	Ground	1	1
24	CML-O	Rx4n	Receiver Inverted Data Output	3	1
25	CML-O	Rx4p	Receiver Non-Inverted Data Output	3	
26		GND	Ground	1	1
27	LVTTL-O	ModPrsL	Module Present	3	
28	LVTTL-O	IntL	Interrupt	3	
29		VccTx	+3.3 V Power Supply transmitter	2	2
30		VccI	+3.3 V Power Supply	2	2
31	LVTTL-I	LPMode	Low Power Mode	3	
32		GND	Ground	1	1
33	CML-I	Tx3p	Transmitter Non-Inverted Data Input	3	
34	CML-I	Tx3n	Transmitter Inverted Data Output	3	
35		GND	Ground	1	1
36	CML-I	Tx1p	Transmitter Non-Inverted Data Input	3	
37	CML-I	Tx1n	Transmitter Inverted Data Output	3	
38		GND	Ground	1	1

Notes:

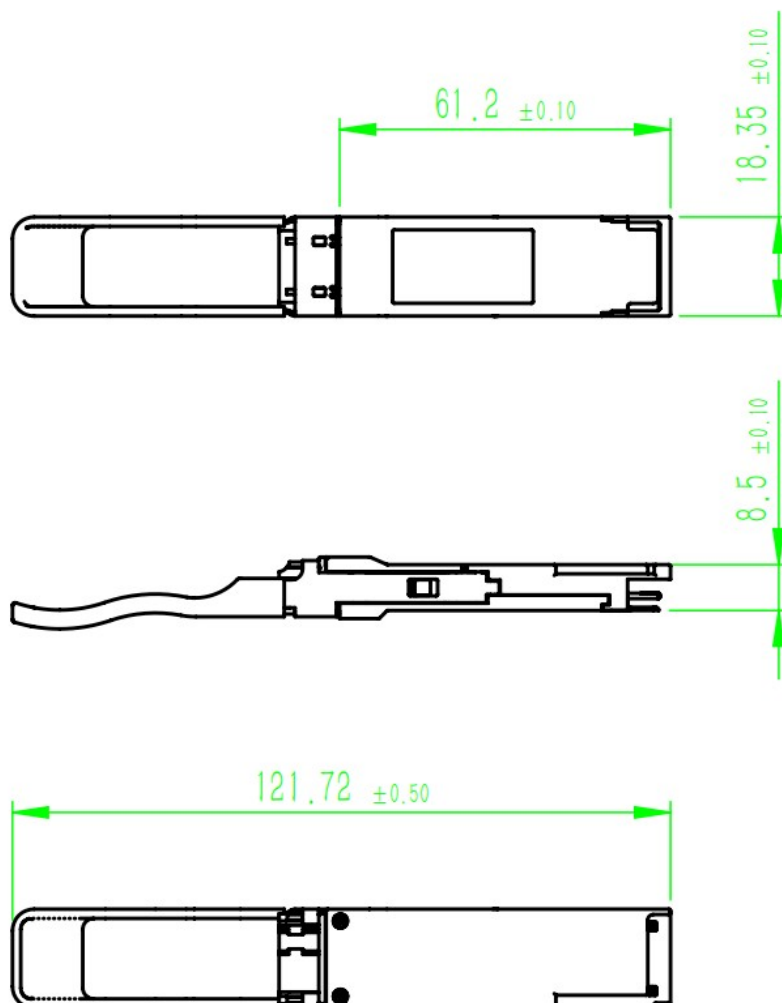
1. GND is the symbol for signal and supply (power) common for QSFP28 modules. All are common within the QSFP28 module and all module voltages are referenced to this potential unless otherwise noted. Connect these directly to the host board signal common ground plane.

2. VccRx, VccI and VccTx are the receiver and transmitter power supplies and shall be applied concurrently. Recommended host board power supply filtering is shown below. VccRx, VccI and VccTx may be internally connected within the QSFP28 transceiver module in any combination. The connector pins are each rated for a maximum current of 1000mA.

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9. Mechanical Dimensions



10. Module Ordering information

PN	Description
OUB8C12412	100G QSFP28 LR1 Bi-Di Tx1291/Rx1311nm 20KM LC DDMI 10~70 °C
OUB8C21412	100G QSFP28 LR1 Bi-Di Tx1311/Rx1291nm 20KM LC DDMI 10~70 °C

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